

Notice of References Cited	Application/Control No. 09/843,178	Applicant(s)/Patent Under Reexamination GOSIOR ET AL.	
	Examiner Aimee J. Li	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Furber, Steve. "ARM System-on-Chip Architecture". Second Edition. Reading, Mass.: Peason Education Limited, ©2000. Pages 19-20, 22-24, 28-32, 216-217, and 271.
	V	
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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